ABSTRACT OF THE DISCLOSURE

An arithmetic unit includes a memory, an arithmetic logic unit, a register and a combining circuit. The arithmetic logic unit executes a predetermined arithmetic operation with respect to the data read from memory. The register temporarily stores the data read from the memory. The combining circuit selects one of the arithmetic logic unit and the register. The combining circuit replaces a part of the data read from the memory with output data received from the selected one of the arithmetic logic unit and the register.